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S18	92	(schedul\$3 with (estimat\$3 predict\$3 ) with cost with (task\$3 process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/02 10:24
L8	8	17 and "718"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/02 10:24

L7	113	(schedul\$3 with (estimat\$3 predict\$3 ) with cost with (task\$3 process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/02 10:24
L2	8	11 and "718"/\$.ccls	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/02 10:24
S1	53	((schedul\$3 near5 (cost\$3 optim\$5 perform\$5 static\$4 dynamic\$4)) same ((application program software task\$3 process\$3) near5 (perform\$5 run\$4 execut\$5 operat\$3))) and (schedul\$3 with static\$4 with ((cost\$3 near4 low\$3) optim\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/02 10:23
L1	53	((schedul\$3 near5 (cost\$3 optim\$5 perform\$5 static\$4 dynamic\$4)) same ((application program software task\$3 process\$3) near5 (perform\$5 run\$4 execut\$5 operat\$3))) and (schedul\$3 with static\$4 with ((cost\$3 near4 low\$3) optim\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/02 10:23



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#### 81 [Techniques for obtaining high performance in Java programs](#)

Iffat H. Kazi, Howard H. Chen, Berdenia Stanley, David J. Lilja

September 2000

**ACM Computing Surveys (CSUR)**, Volume 32 Issue 3

Full text available: [pdf\(816.13 KB\)](#)

Additional Information: [full citation](#), [abstract](#)

This survey describes research directions in techniques to improve the performance of programs written in the J extensive portability of programs. A Java interpreter dynamically executes Java bytecodes, which comprise the i improved through compilation, possibly at the expense of portabili ...

**Keywords:** Java, Java virtual machine, bytecode-to-source translators, direct compilers, dynamic compilation,

#### 82 [Cellular disco: resource management using virtual clusters on shared-memory multiprocessors](#)

Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum

August 2000

**ACM Transactions on Computer Systems (TOCS)**, Volume 18 Issue 3

Full text available: [pdf\(287.05 KB\)](#)

Additional Information: [full citation](#), [abstract](#)

Despite the fact that large-scale shared-memory multiprocessors have been commercially available for several y and cost of making the required changes to the operating system. A recently proposed approach, called Disco, s operating system technology. In this paper we present a ...

**Keywords:** fault containment, resource managment, scalable multiprocessors, virtual machines

#### 83 [Binary translation and architecture convergence issues for IBM system/390](#)

Michael Gschwind, Kemal Ebcioglu, Erik Altman, Sumedh Sathaye

May 2000

**Proceedings of the 14th international conference on Supercomputing**

Full text available: [pdf\(1.44 MB\)](#)

Additional Information: [full citation](#), [abstract](#)

We describe the design issues in an implementation of the ESA/390 architecture based on binary translation to : decomposed into instruction "primitives" which are then scheduled onto a wide-issue machine. The aim is to act can be exploited by binary translation software ...

#### 84 [Practicing JUDO: Java under dynamic optimizations](#)

Michał Cierniak, Guei-Yuan Lueh, James M. Stichnoth

May 2000

**ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2000 conference on Pr**

Full text available:  pdf(190.06 KB)


Additional Information: [full citation](#), [abstract](#)

A high-performance implementation of a Java Virtual Machine (JVM) consists of efficient implementation of Just-In-Time compilation and garbage collection. These components are tightly coupled to achieve high performance. In this paper, we present some static and dynamic analysis of the Java Virtual Machine (JVM), ...

85 [Session summaries from the 17th symposium on operating systems principle \(SOSP'99\)](#)

Jay Lepreau, Eric Eide

April 2000 **ACM SIGOPS Operating Systems Review**, Volume 34 Issue 2

Full text available:  pdf(3.15 MB)

Additional Information: [full citation](#), [index terms](#)

86 [System-level power optimization: techniques and tools](#)

Luca Benini, Giovanni de Micheli

April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 2

Full text available:  pdf(385.22 KB)


Additional Information: [full citation](#), [abstract](#)

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic systems that consume energy, namely computation, communication, and storage units, and we review methods of reducing the energy consumption of electronic systems. This survey ...

87 [Overcoming the challenges to feedback-directed optimization \(Keynote Talk\)](#)

Michael D. Smith

January 2000 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN workshop on Dynamic Binary Translation**

Full text available:  pdf(1.33 MB)

Additional Information: [full citation](#), [abstract](#)

*Feedback-directed optimization (FDO) is a general term used to describe any technique that alters a program's behavior in FDO and discusses the challenges inhibiting further acceptance of these techniques. It also argues that immutable executables and traditional static optimizations are ...*

88 [A code-motion pruning technique for global scheduling](#)

Luiz C. V. Dos Santos, M. J. M. Heijligers, C. A. J. Van Eijk, J. Van Eijnhoven, J. A. G. Jess

January 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 2

Full text available:  pdf(293.27 KB)

Additional Information: [full citation](#), [abstract](#)


In the high-level synthesis of ASICs or in the code generation for ASIPs, the presence of conditionals in the behavior of the code can be a problem. Such a way that the search space is limited by the applied heuristics. For example, they might miss opportunities for code motion. This paper presents a method which allows generalized code motions. Scheduling ...

**Keywords:** code generation, code motion, global scheduling, high-level synthesis, speculative execution

89 [Cellular Disco: resource management using virtual clusters on shared-memory multiprocessors](#)

Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum

December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth ACM symposium on Operating Systems Principles**

Full text available:  pdf(1.93 MB)

Additional Information: [full citation](#), [abstract](#)

Despite the fact that large-scale shared-memory multiprocessors have been commercially available for several years, the cost of making the required changes to the operating system. A recently proposed approach, called Disco, is a new operating system technology. In this paper we present a system ...

90 [Static scheduling algorithms for allocating directed task graphs to multiprocessors](#)

Yu-Kwong Kwok, Ishfaq Ahmad

December 1999 **ACM Computing Surveys (CSUR)**, Volume 31 Issue 4



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### 101 [Space/time-efficient scheduling and execution of parallel irregular computations](#)

Tao Yang, Cong Fu  
November 1998

**ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 20 I:

Full text available: [pdf\(374.95 KB\)](#)

Additional Information: [full citation](#), [abstract](#)

In this article we investigate the trade-off between time and space efficiency in scheduling and executing parallel model irregular parallelism with mixed granularity, and we use direct remote memory access to support fast cor improve memory utilization while retaining good time efficiency, and we ...

**Keywords:** DAG scheduling, direct remote memory access, irregular parallelism, run-time support

### 102 [Space-time scheduling of instruction-level parallelism on a raw machine](#)

Walter Lee, Rajeev Barua, Matthew Frank, Devabhaktuni Srikrishna, Jonathan Babb, Vivek Sarkar, Saman Amarasekera  
October 1998

**Proceedings of the eighth international conference on Architectural support for programming languages and operating systems**

Full text available: [pdf\(1.79 MB\)](#)

Additional Information: [full citation](#), [abstract](#)

Increasing demand for both greater parallelism and faster clocks dictate that future generation architectures will A Raw microprocessor distributes all of its resources, including instruction streams, register files, memory ports, Because communication in Raw machines is distributed, com ...

### 103 [Automatic data layout for distributed-memory machines](#)

Ken Kennedy, Ulrich Kremer  
July 1998

**ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 20 I:

Full text available: [pdf\(633.20 KB\)](#)

Additional Information: [full citation](#), [abstract](#)

The goal of languages like Fortran D or High Performance Fortran (HPF) is to provide a simple yet efficient machine intellectual challenge in writing an efficient program in such languages. The performance of a data layout depends on processors. This makes the choice of a good layout extremely ...

**Keywords:** high performance Fortran

### 104 [Models and languages for parallel computation](#)

David B. Skillicorn, Domenico Talia  
June 1998

**ACM Computing Surveys (CSUR)**, Volume 30 Issue 2